

ATMES

SOME/IP Simulation

The new `somelpSim` class library is a low cost solution to build residual bus simulation systems for SOME/IP. Based on a standard PC with Ethernet interface, Visual Studio development environment and the `somelpSim` class library both simple as well as complex residual bus simulations may be generated.

The class library interfaces are available in C/C++ programming language. Other programming interfaces (C#, Java, Python) are available on request. Included are several software examples to facilitate the integration into custom applications.

The additional available `RbsGenerator` (see below) may be used to automatically generate a residual bus simulation out of a FIBEX data base file.

Applications

- Simulation
- Rapid Prototyping
- SIL
- Evaluation

Standards supported:

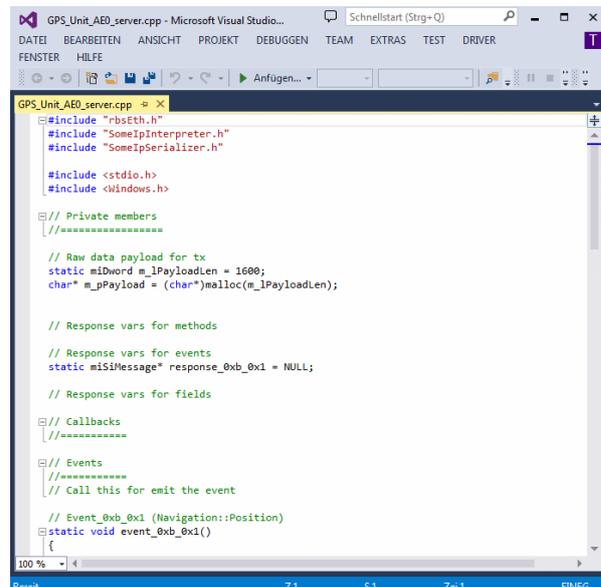
- SOME/IP
- Service Discovery

System requirements

Windows™ 7, Windows™ 10, Linux. RTOS on request

Advantages

- Integration into Visual Studio
- Automatic generation out of FIBEX files



```
GPS_Unit_AE0_server.cpp
#include "psfth.h"
#include "SomeIpInterpreter.h"
#include "SomeIpSerializer.h"
#include <stdio.h>
#include <windows.h>

// Private members
//=====

// Raw data payload for tx
static mDword m_IPayloadLen = 1600;
char* m_pPayload = (char*)malloc(m_IPayloadLen);

// Response vars for methods
// Response vars for events
static mSiMessage* response_0xb_0x1 = NULL;
// Response vars for fields

// Callbacks
//=====

// Events
//=====
// Call this for emit the event
// Event_0xb_0x1 (Navigation::Position)
static void event_0xb_0x1()
{
}
```

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Analyzing Option for SOME/IP Simulation

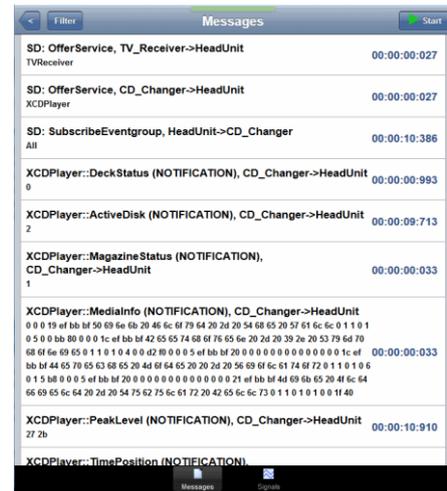
The Analyzing Option for someIpSim is a class library to display transmitted and received messages inside a Web browser. SOME/IP as well as Service-Discovery messages are displayed. In addition individual signals may be shown as values or in a diagram.

System requirements for the class library

Windows™ 7, Windows™ 10. Linux on request

System requirements User Interface

Windows™, Android, iOS, Linux



Generator Option for SOME/IP Simulation

The RbsGenerator is a software application to generate to automatically generate a residual bus simulation out of a FIBEX data base file. It generates directly executable Visual Studio Projects.

Using the data security option data base contents are converted into binary files. Therefore together with an Ethernet-RBS no human readable data base files have to delivered respectively included.

During Generation of the Ethernet-RBS additional function modules may be included. With that solution it is a simple task to generate compilable source code for SOME/IP requests. In addition the administration and reuse of the source code modules becomes very easy.

Supported Standards:

- Visual Studio 2013
- FIBEX 4

System requirements

Windows™ 7, Windows™ 10

